## WAIT FREE SYNCHRONIZATION Maurice Herlihy, ACM TOPLAS, Jan 1991

Original slides by Tengyu Ma, 2010

Lightly updated by John Mellor-Crummey 21 March 2019

Friday, November 5, 2010

### OUTLINE

- Motivation
- Wait-free object model
- Consensus problem
- Wait-free solutions to the consensus problem
- Impossibility proofs
- Universal construction

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- Wait-free solutions to the consensus problem
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### MOTIVATION

Concurrent objects in shared memory

- Traditional approach: mutual exclusion using locks
- Some problems with mutual exclusion
  - no fault tolerance
    - a thread may fail in the critical section
  - a slow thread may delay others

### OBJECTS WITHOUT WAITING?

New approach: wait-free concurrent object
a thread can proceed independent of others
Questions:

• what wait-free objects are impossible?

• how can we implement wait-free objects?

### THE MAIN PROBLEM

- Given two concurrent objects X, Y.
- Is it possible to implement X by using Y?

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#### WAIT FREE CONCURRENT OBJECTS

Definition: A concurrent object is wait-free if every thread completes a method in finite number of steps

# THE MAIN QUESTION

- How to implement concurrent object X by Y?
- Previous work
  - from single-writer single-reader boolean safe register, we can build multi-writer multireader atomic register
- This paper shows that an atomic register is a weak concurrent object

#### UNDERSTANDING THE POSSIBILITIES

- Theorem: It is impossible to build a wait-free queue from atomic registers
- How can one prove theorem like this?
- Basic idea:
  - determine a consensus number for each type of concurrent object
  - show that objects with low consensus number cannot implement ones with high consensus number

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# CONSENSUS PROBLEM

- Suppose there are n threads
- Each thread starts with an input value
- By executing some protocol, each outputs a value
- Three requirements:
  - Consistency: all threads decide the same value
  - Wait free: every thread eventually decides some value
  - Validity: the value decided is from the set of inputs

#### PARAMETERS FOR CONSENSUS

- Two factors that should be specified
  - What shared data-structure is used?
  - How many threads?

## CONSENSUS NUMBER



Supports n-thread consensus protocol

The <u>consensus number (CN)</u> for object type X is the largest number n, for which there exists a consensus protocol of n threads using objects of type X and atomic registers.

# CONSENSUS NUMBER

- Consensus number measures synchronization power
- Classify objects by consensus number (CN)
  - objects with different CN in different classes
  - object with CN N cannot implement any objects with CN of M > N.

# CONSENSUS HIERARCHY

consensus number	Objects					
1	register					
2	test&set, swap, fetch&add, queue,stack					
•••	•••••					
2n-2	n-register assignment					
••••	•••••					
$\infty$	memory to memory move and swap, augmented queue, compare&swap,fetch&cons, sticky byte					

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Universal construction

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- Proof
  - Queue initially with two entries 0,1
  - Two shared **atomic** registers prefer[0], prefer[1]

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**Algorithm 1** Algorithm for  $P_i$  with input value  $v_i$ 

1: prefer[i] :=  $v_i$ 2: if deque() = 0 then 3: return prefer[i] 4: else 5: return prefer[1-i] 6: end if

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If deque() = 1, the thread can always read the value written by the other. Why?

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- Proof
  - Queue initially with two entries 0,1
  - Two shared **atomic** registers prefer[0], prefer[1]

Is it wait-free ?

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4: **else** 

- 5: return prefer[1-i]
- 6: **end if**

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Algorithm 1 Algorithm for  $P_i$ Require:  $P_i$  has input  $v_i$ 1: enq( $v_i$ ) 2: return peek()

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Theorem: Augmented Queue has infinite consensus number

Algorithm 1 Algorithm for  $P_i$ Require:  $P_i$  has input  $v_i$ 1: enq $(v_i)$ 2: return peek()

Is it wait-free?

**Definition**(Multiple Assignment): The expression  $r_1, r_2, \ldots, r_n := v_1, \ldots, v_n$ atomically assign each value  $v_i$  to each register  $r_i$ 

**Theorem:** Registers with atomic *m*-assignment have consensus number at least *m* 

#### N-REGISTER ASSIGNMENT CONT'D

 $p_1$ 

 $r_{23}$ 

 $r_{12}$ 

 $p_2$ 

 $r_2$ 

 $r_{15}$ 

 $r_{13}$ 

 $p_3$ 

 $r_3$ 

 $r_{14}$ 

 $r_5$ 

 $p_4$ 

 $p_5$ 

#### **Proof**:

- Each thread has a singlewriter register.
- Each two threads share a multi-writer register

**Algorithm 1** Algorithm for  $P_i$ 

- 1: atomically assign  $r_i, r_{i1}, r_{i2}, \ldots, r_{in} := v_i, \ldots, v_i$
- 2: **return** determineFirstAssignment()

 $p_1$ 

 $p_3$ 

 $p_2$ 

 $p_5$ 

 $p_4$ 

**Algorithm 1** Algorithm for  $p_i$ 

- 1: atomically assign  $r_i, r_{i1}, \ldots, r_{in} := v_1, \ldots, v_i$
- 2: return determineFirstAssignment()

Algorithm 2 determineFirstAssignment

for all  $1 \le i < j \le n$  do determineOrder(i,j) end for

Algorithm 3 determineOrder(i,j)

**Ensure:** determine the order between occurred assignment

- 1: if  $r_{ij}$  has not been initialized then
- 2: assignments by  $p_i, p_j$  has not occurred.
- 3: else if  $r_i$  is not initialized but  $r_j$  is initialized then
- 4:  $p_j$  precedes  $p_i$
- 5: else if  $r_j$  is not initialized but  $r_i$  is initialized then
- 6:  $p_i$  precedes  $p_j$
- 7: else
- 8: if  $r_i = r_{ij}$  then

```
9: p_j precedes p_i
```

```
11: p_i precedes p_j
```

```
12: end if
```

```
13: end if
```

 $v_1$ 

2)-

 $p_3$ 

U-

 $p_5$ 

 $p_4$ 

 $p_1$ 

 $v_1$ 

 $p_2$ 

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- 12: **end if**
- 13: end if

 $v_1$ 

 $v_1$ 

 $p_2$ 

 $v_1$ 

 $\mathcal{V}^{-}$ 

 $p_3$ 

 $v_3$ 

 $p_5$ 

 $v_3$ 

 $p_4$ 

 $v_3$ 

 $v_3$ 

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 $v_5$ 

 $p_5$ 

 $v_3$ 

 $v_5$ 

 $v_3$ 

 $v_5$ 

 $p_4$ 

 $v_5$ 

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 $p_3$ 

 $v_3$ 

 $p_1$ 

 $v_3$ 

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 $p_2$ 

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# IMPOSSIBILITY RESULTS

Proof Terminology

- Protocol state: The states of all the concurrent objects and the internal states of the algorithms run in every processes
- A state is *bivalent* if starting from this state, any decision is still possible.
- A state is *x-valent* if starting from this state, the only possible decision value is x.
- A state is *univalent* if it is x-valent for some value x

#### EXAMPLES

- A state is *x-valent* if starting from this state, the only possible decision value is x.
- A state is *bivalent* if starting from this state, either decision is still possible.
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Alg	gorithm 1	Algorithm	for	$P_i$	with	input	value	$v_i$
1:	prefer[i] :=	$v_i$						
2:	<pre>if deque()</pre>	= 0 then						
3:	return	prefer[i]						
4:	else							
5:	return	prefer[1-i]						
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1: $\operatorname{prefer}[i] := v_i$ 2: $\operatorname{if} \operatorname{deque}() = 0$ then 3: $\operatorname{return} \operatorname{prefer}[i]$ 4: $\operatorname{else}$ 5: $\operatorname{return} \operatorname{prefer}[1\text{-}i]$ 6: $\operatorname{end} \operatorname{if}$	<b>Algorithm 1</b> Algorithm for $P_i$ with input value $v_i$
<ol> <li>2: if deque() = 0 then</li> <li>3: return prefer[i]</li> <li>4: else</li> <li>5: return prefer[1-i]</li> <li>6: end if</li> </ol>	1: $\operatorname{prefer}[i] := v_i$
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6: end if	5: return $prefer[1-i]$
	6: end if



- A decision step is an operation which carries the protocol from a bivalent state to a univalent state.
- Proposition: There exists a state, such that every feasible operation on it is a decision step.
- The state should be reachable from the initial state.

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	(
	Some initial
	state is
	bivalent
initial	$\sim$
state	
P1's opera	tion
bivale	nt

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Finally reach a critical state!



## IMPOSSIBILITY RESULTS

- **Theorem**: atomic registers cannot simulate 2-processes consensus protocol.
- Proof structure: Assume that there exists a protocol. Find the critical state (the state for which every operation on it is decision step).
- Enumerate all the possible cases of the operations following this state.



- Consider: Two operations following the critical state are on different registers.
- Trivial since the two operations on different objects can be commuted without changing the final state.
- Every feasible operation on the critical state is on the same base object (register).



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bivalent.

**Every following** 

state univalent

critical

state: S

State S3

Predit

Quartite P.

x-valent

Quatite D

Predit

y-valent

- Consider: Two operations following the critical state are on different registers.
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- Every feasible operation on the critical state is on the same base object (register).
   Contradiction Contradiction Contradiction object (register).
   Contradiction on the same base impossibility proof

Two operations on the same register.

1. <u>one of the</u> <u>operations is read</u>

2. each of the operations is write



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## UNIVERSALITY RESULTS

- Every object with consensus number n, can implement any other concurrent object within a system of n threads
- Consensus object
  - a consensus protocol with a register where the decision value is written
  - has a function decide(value: input). a thread calls decide to invoke the consensus protocol and get the decision value as result.
  - every object with consensus number n can implement the consensus object within a system of n threads.

## UNIVERSALITY RESULTS

- Implement a concurrent object by consensus objects and atomic registers
- General idea: An execution of a concurrent object can be presented as a linked list of cells.

#### REPRESENTING À CONCURRENT OBJECT

- The general idea: An execution of a concurrent object can be presented as a linked list of cells.
- A cell has the following fields
  - seq: sequence number indicating the order of the operations. Increase by 1 for successive cells
  - inv: invocation (operation name, argument name)
  - new-state: the new state of the object
  - new-result: the result value of the operation
  - before, after: point to the cell previous and next to it.

seq	
inv	
new-state	
result	
after	
before	

a basic

cell

## A LINKED LIST OF CELLS



- When a thread invokes an operation, it creates a cell with operation information and sequence number 0.
- Maintains a linked list of cells







- We say a thread *threads a cell* if it adds the cell into the linked list.
- Naive idea: use a consensus protocol to decide which cell should be threaded next.







Friday, November 5, 2010

- We say a process *threads a cell* if it adds the cell into the linked list.
- Naive idea: use a consensus protocol to decide which cell should be threaded next.



P1's next operation:

peek()

seq=0

peek()

undecided

undecided

undecided



 Then add the decided cell into the linked list







# Then add the decided cell into the linked list



- Then add the decided cell into the linked list
- update fields of the cell



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Two problems:a) Not wait free.b) Consensus object can used only once.



P1's next operation:

enq(t)

seq=6

enq(t)

[y,t]

## Solution:

 an array of atomic registers head[] pointing to the latest cell each process has seen
 an array of atomic registers announce[] pointing to cells to be threaded



P2's next operation: enq(z) seq=0 enq(z) undecided undecided undecided undecided







- •Make the after pointer a consensus object
- •The call c.after.decide() will return the decision value of the consensus and write the decision value to c.after





enq(z)seq=0 enq(z)undecided undecided undecided undecided

```
initialize the cell with seq = 0
let announce[P] point to it.
head[P] = max\{head[1], \dots, head[n]\}
while announce [P].seq = 0 do
  c = head[P]
  h = announce[c.seq mod n + 1]
  if h.seq = 0 then
    prefer = h
  else
    prefer = announce[P]
  end if
  d = c.after.decide(prefer)
  d.seq = c.seq + 1
  update the field of d according to c.inv, c.new-state
  head[P] = d
end while
return announce[P].result
```

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    prefer = announce[P]
  end if
  d = c.after.decide(prefer)
  d.seq = c.seq + 1
  update the field of d according to c.inv, c.new-state
  head[P] = d
end while
return announce[P].result
```

seq = 0 indicates
that the cell has not
been threaded

```
initialize the cell with seq = 0
let announce [P] point to it.
head[P] = max\{head[1], \dots, head[n]\}
while announce [P].seq = 0 do
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  update the field of d according to c.inv, c.new-state
  head[P] = d
end while
return announce [P].result
```

make the head be as close to the end of the list as possible

```
initialize the cell with seq = 0
let announce[P] point to it.
head[P] = \max\{head[1], \dots, head[n]\}
while announce [P].seq = 0 do
  c = head[P]
  h = announce[c.seq mod n + 1]
  if h.seq = 0 then
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  end if
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  update the field of d according to c.inv, c.new-state
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end while
return announce[P].result
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actually it is a loop. just for brevity. no atomicity requirement

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  head[P] = d
end while
return announce [P].result
```

The main loop. iterates as long as the cell is not threaded

initialize the cell with seq = 0let announce[P] point to it.  $head[P] = max\{head[1], \dots, head[n]\}$ while announce [P].seq = 0 do c = head[P]h = announce[c.seq mod n + 1]if h.seq = 0 then prefer = helse prefer = announce[P]end if d = c.after.decide(prefer)d.seq = c.seq + 1update the field of d according to c.inv, c.new-state head[P] = dend while return announce[P].result

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while announce [P].seq = 0 do
  c = head[P]
  h = announce [c.seq mod n + 1]
  if h.seq = 0 then
    prefer = h
                                                    h is the cell that the
  else
                                                    thread tries
    prefer = announce[P]
                                                    to help when its
  end if
                                                    head pointer points
  d = c.after.decide(prefer)
  d.seq = c.seq + 1
                                                    to c
  update the field of d according to c.inv, c.new-state
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while announce [P].seq = 0 do
  c = head[P]
  h = announce[c.seq mod n + 1]
  if h.seq = 0 then
    prefer = h
  else
    prefer = announce[P]
  end if
  d = c.after.decide(prefer)
  d.seq = c.seq + 1
  update the field of d according to c.inv, c.new-state
  head[P] = d
end while
return announce[P].result
```

```
initialize the cell with seq = 0
let announce [P] point to it.
head[P] = max\{head[1], \dots, head[n]\}
while announce [P].seq = 0 do
  c = head[P]
  h = announce[c.seq mod n + 1]
  if h.seq = 0 then
    prefer = h
  else
    prefer = announce[P]
  end if
                                                       help, or if h has not
  d = c.after.decide(prefer)
                                                       yet been threaded.
  d.seq = c.seq + 1
  update the field of d according to c.inv, c.new-state
  head[P] = d
end while
return announce [P].result
```

check if h needs

```
initialize the cell with seq = 0
let announce[P] point to it.
head[P] = max\{head[1], \dots, head[n]\}
while announce [P].seq = 0 do
  c = head[P]
  h = announce[c.seq mod n + 1]
  if h.seq = 0 then
    prefer = h
  else
    prefer = announce[P]
  end if
  d = c.after.decide(prefer)
  d.seq = c.seq + 1
  update the field of d according to c.inv, c.new-state
  head[P] = d
end while
return announce[P].result
```

otherwise try to

thread own cell

```
initialize the cell with seq = 0
let announce [P] point to it.
head[P] = max\{head[1], \dots, head[n]\}
while announce [P].seq = 0 do
  c = head[P]
  h = announce[c.seq mod n + 1]
  if h.seq = 0 then
    prefer = h
  else
    prefer = announce[P]
  end if
  d = c.after.decide(prefer)
  d.seq = c.seq + 1
  update the field of d according to c.inv, c.new-state
  head[P] = d
end while
return announce [P].result
```

Observe that c.after is a consensus object and however many times decide() is called, the return value is the same.

```
initialize the cell with seq = 0
let announce[P] point to it.
head[P] = max\{head[1], \dots, head[n]\}
while announce [P].seq = 0 do
  c = head[P]
  h = announce[c.seq mod n + 1]
  if h.seq = 0 then
    prefer = h
  else
    prefer = announce[P]
  end if
  d = c.after.decide(prefer)
  d.seq = c.seq + 1
  update the field of d according to c.inv, c.new-state
  head[P] = d
end while
return announce[P].result
```

```
initialize the cell with seq = 0
let announce[P] point to it.
head[P] = max\{head[1], \dots, head[n]\}
while announce [P].seq = 0 do
  c = head[P]
  h = announce[c.seq mod n + 1]
  if h.seq = 0 then
    prefer = h
  else
    prefer = announce[P]
  end if
  d = c.after.decide(prefer)
  d.seq = c.seq + 1
  update the field of d according to c.inv, c.new-state
  head[P] = d
end while
return announce[P].result
```

```
initialize the cell with seq = 0
let announce [P] point to it.
head[P] = max\{head[1], \dots, head[n]\}
while announce [P].seq = 0 do
  c = head[P]
  h = announce[c.seq mod n + 1]
  if h.seq = 0 then
    prefer = h
  else
                                          However many times d is
    prefer = announce[P]
                                         updated by different processes,
  end if
                                         the result is the same!!
  d = c.after.decide(prefer)
  d.seq = c.seq + 1
  update the field of d according to c.inv, c.new-state
  head[P] = d
end while
return announce[P].result
```

Friday, November 5, 2010

```
initialize the cell with seq = 0
let announce[P] point to it.
head[P] = max\{head[1], \dots, head[n]\}
while announce [P].seq = 0 do
  c = head[P]
  h = announce[c.seq mod n + 1]
  if h.seq = 0 then
    prefer = h
  else
    prefer = announce[P]
  end if
  d = c.after.decide(prefer)
  d.seq = c.seq + 1
  update the field of d according to c.inv, c.new-state
  head[P] = d
end while
return announce[P].result
```

```
initialize the cell with seq = 0
let announce[P] point to it.
head[P] = max\{head[1], \dots, head[n]\}
while announce [P].seq = 0 do
  c = head[P]
  h = announce[c.seq mod n + 1]
  if h.seq = 0 then
    prefer = h
  else
    prefer = announce[P]
  end if
  d = c.after.decide(prefer)
  d.seq = c.seq + 1
  update the field of d according to c.inv, c.new-state
  head[P] = d
end while
return announce[P].result
```





ann[1]

















head[1]

head[2]

```
initialize the cell with seq = 0
let announce[P] point to it.
head[P] = max\{head[1], \dots, head[n]\}
while announce [P].seq = 0 do
  c = head[P]
  h = announce[c.seq mod n + 1]
  if h.seq = 0 then
    prefer = h
  else
    prefer = announce[P]
  end if
  d = c.after.decide(prefer)
  d.seq = c.seq + 1
  update the field of d according to c.inv, c.new-state
  head[P] = d
end while
return announce[P].result
```














```
initialize the cell with seq = 0
let announce[P] point to it.
head[P] = max\{head[1], \dots, head[n]\}
while announce [P].seq = 0 do
  c = head[P]
  h = announce[c.seq mod n + 1]
  if h.seq = 0 then
    prefer = h
  else
    prefer = announce[P]
  end if
  d = c.after.decide(prefer)
  d.seq = c.seq + 1
  update the field of d according to c.inv, c.new-state
  head[P] = d
end while
return announce[P].result
```







```
initialize the cell with seq = 0
let announce[P] point to it.
head[P] = max\{head[1], \dots, head[n]\}
                                                                                        ann[2]
while announce [P].seq = 0 do
  c = head[P]
  h = announce[c.seq mod n + 1]
  if h.seq = 0 then
    prefer = h
                                                                                        ann[1]
  else
    prefer = announce[P]
  end if
  d = c.after.decide(prefer)
  d.seq = c.seq + 1
  update the field of d according to , c.new-state
  head[P] = d
end while
return announce[P].result
```

















head[2]



head[1]



head[2]



head[1]

















head[1]

head[2]

























## PROOF OF THE CORRECTNESS

#### Observations

- non-zero sequence number indicates successful threading
- the consensus protocols guarantee that the fields of the cells will not be updated with different values.
- at cell with sequence number k, every thread tries to help thread (k+1) mod n
- if a cell is announced by thread k+1, after at most n more cells have been threaded
  - everyone will check if process k+1 needs help
  - everyone will help

### MORE PRACTICAL CONSTRUCTIONS

- New universal construction
  - P. Choung, F. Ellen, V. Ramachandran "A universal construction for wait-free transaction friendly data structure".
  - implements any shared data structure with θ(s+p) space, where s is the size of the shared data structure and p is the number of processes.
  - uses only CAS and registers as base objects.
- Ad-hoc wait-free data structures
  - lower overhead by using a purpose-built construction

### CONCLUSIONS

#### Wait-free synchronization is possible, practical, and useful!

# REFERENCES

- Maurice Herlihy, "Wait-free synchronization"
- Lynch and Tuttle, "An Introduction to Input/ Output Automata"
- Maurice Herlihy & Nir Shavit "Lecture notes of Art of multiprocessor computing.
- L. Lamport "How to make a multiprocessor computer that correctly executes multiprocess programs."

#### LINEARIZABILITY VS SEQUENTIAL CONSISTENCY

- Linearizability is stronger than sequential consistency.
- sequential consistency is not composable:(not a local property)
  - If two objects are both sequential consistent, the composition of them might be not.
- linearizability has composability.
  - We only need to study isolated object